AMENDMENTS TO THE SPECIFICATION:

A substitute specification is provided herewith to facilitate prosecution of the application. Additionally, a marked reproduction of the original specification, showing changes effected in the substitute specification, is submitted herewith.

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SUBSTITUTE SPECIFICATION

FIG. 14 structure of an event-time protocol;

FIG. 15 an example of a formation basis for formal instruction names;

FIG. 16 an example of the definition of application instructions;

FIG. 17 an example of a blocking list managed in a control;

FIG. 18 an example of the determination of error instructions;

FIG. 19 an example of a control with more complex functions;

FIG. 20 structure and name definition according to FIG. 19;

FIG. 21 all data for an instruction library of an instruction computer according to FIGS. 19 and 20; and

FIG. 22 features of an embodiment as small-scale control.

DETAILED DESCRIPTION OF THE INVENTION

FIG. 1 shows the basic classification of the functional ranges in the structure 1 of the new control. The time-critical functions of the desired signal/actual signal comparison, reactions to deviations of the actual compared to the desired state and the activation of state-changing actuators according to instructions are assigned to the execution computer 2. Instructions received from the instruction computer 3 are processed by the execution computer 2 without check. As a result, the execution of an instruction and the reaction to deviations of the actual compared to the desired state are realized autonomously by the execution computer 2. It is useful, or even compulsory to reach shortest

SUBSTITUTE SPECIFICATION

Accompanying the specification are figures which assist in illustrating the embodiments of the invention, in which:

FIG. 1a representation of a basic classification of the functional ranges in the structure of the control;

FIG. 2a hierarchically classified functional structure of a technical system;

FIG. 3 information to be defined for the elementary functions on the basis of a general example;

FIG. 4a simple technical system in a schematic representation;

FIG. 5a functional structure according to FIG. 4;

FIG. 6a definition of the elementary functions according to FIG. 4;

FIGURES 7A-7B

~~FIG. 7a~~ representation of input and structure of a data frame to realize the control;

FIG. 8a structure of an execution computer;

FIG. 9a content of an instruction as instruction set for the instruction buffer;

FIGURES 10A-10B

~~FIG. 10a~~ representation of the function of an instruction starter;

FIG. 11a representation of the function of an EF-controler;

FIG. 12a representation of the function of a not-desired state evaluator;

FIG. 13a representation of the function of a state monitor;